

**ELECTRONICS ENGINEERING DEPARTMENT  
SVNIT, SURAT**

Final List of CCMT Applicant Candidates Selected for Admission to two Courses in this Department against the existing vacancies [OPEN & OBC for M.Tech. in Communication Systems, while SC for VLSI & Embedded Systems] is given below.

<b>Sr. No</b>	<b>Name</b>	<b>Gate Score</b>	<b>Category</b>	<b>Remarks</b>
1	VIDUSHI TIWARI	555	OPEN	Confirmed
2	YOGESH CHAURASIYA	555	OBC	Confirmed
3	SHARAD JOSHI	549	OPEN	Confirmed
4	SUCHAK KEVAL PRAKASHKUMAR	500	OPEN	Confirmed

<b>Sr. No</b>	<b>Name</b>	<b>Gate Score</b>	<b>Category</b>	<b>Remarks</b>
1	TANDEL JITENKUMAR ANANDBHAI	494	OBC	Confirmed
2	VIVEK SINGH	494	OBC	Confirmed
3	SAMMETA GOPI KRISHNA	476	OBC	Confirmed

<b>Sr. No</b>	<b>Name</b>	<b>Gate Score</b>	<b>Category</b>	<b>Remarks</b>
1	MAKWANA MUKUND NATWARBHAI	366	SC	Confirmed

B. R. Taunk  
PG Incharge,  
Communication Systems

A. D. Darji  
PG Incharge,  
VLSI & Embedded Systems

P. K. Shah  
Head,  
ECED