# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
## EXAMINATION BRANCH :: KAKINADA – 500 033
## M.Tech I Sem. ( 08 Admitted Batches only) Supplementary Examinations  Apri,2012
## T I M E T A B L E

**Time: 10.00 AM to 1.00 PM**

| BRANCHES/ Specializations | 16.04.2012 Monday | 18.04.2012 Wednesday | 20.04.2012 Friday | 23.04.2012 Monday | 25.04.2012 Wednesday | 27.04.2012 Friday | 30.04.2012 Monday | 02.05.2012 Wednesday | 04.05.2012 Friday |
|---|---|---|---|---|---|---|---|---|---|
| CIVIL (73) | Industrial Structures | Remote Sensing & GIS in Civil Engineering | Urban Hydrology Storm Drainage and Management | Geo-Technique for Infrastructures | Urban Transport Alternative | Sea and inland port infrastructures | -- | -- | -- |
| MECHANICAL CAD/CAM (04) | Numerical Methods for partial differential Equation | Computer Aided Design | Stress Analysis & Vibration | Finite Element Methods | Design Optimization | **Elective – I** Design for Manufacturing / Mechatronics / Manufacturing Methods & Mechanics of Composites | -- | -- | -- |
| MECHANICAL MACHINE DESIGN (15) | Advanced Mechanics | Advanced Mechanics of Solids | Creep, Fatigue & Fracture Mechanics | Finite Element Methods | Computational Methods | **Elective-I** Pressure Vessel Design / Mechanics of Composite Materials / Theory of Plasticity | -- | --- | --- |
| C S E COMPUTER SCIENCE ENGG. & COMPUTER SCIENCE (58) & (05) | Design & Analysis of algorithms | Computer Communications | Computer Organization | Database Management Systems | Operating systems | Software Engineering | -- | -- | -- |
| C S E INFORMATION TECHNOLOGY (40) | Middle Ware Technologies | Network Programming | S/W Project Management | Data ware housing & Mining | Network Security & Cryptography | Web Technologies | -- | -- | -- |

| Program | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| C S E NEURAL NETWORKS (69) | Design & Analysis of algorithms | Computer Communication | Computer Organization | Database Management Systems | Introduction to artificial Intelligence | Introduction Neural Network | -- | -- | -- |
| C S E SOFTWARE ENGINEERING (25) | Wireless Networks & Mobile Computing | Unix Network Programming | S/ W Requirement & Estimation | Software Process & Project Management | Object oriented Modeling | Web-Technologies | -- | -- | -- |
| E C E DECS (38) | Advanced Digital Signal Processing | Elective – I Artificial Intelligence | Digital Data Communication | Elective – II Embedded & Real Time Systems | Digital System Design | Detection & Estimation of Signals | Elective – I Advanced Computer Architecture | Elective-II Neural Networks and Applications | Advanced Operating System |
| E C E DSCE (06) | -- | VLSI Technology & Design | Elective – I Digital Data Commutations | -- | Digital System Design | -- | Advanced Computer Architecture | Neural Networks and Applications | Advanced Operating System |
| E C E ECE (70) | Advanced Digital Signal Processing | VLSI Technology and Architecture Design Methodologies | Digital Data Communications | Coding Theory and Practice | Statistical Signal Processing | VHDL Modeling of Digital Systems | --- | --- | -- |
| E C E ES (55) | Embedded Systems Concepts | VLSI Technology and Design | Digital Data Communications, Image Video Processing | Micro Controller for Embedded System Design | --- | System Modeling & Simulation | Elective-I Advanced Computer Architecture / Algorithm Analysis & Design | Analog and Digital IC Design | -- |
| E C E SSP (45) | Advanced Digital Signal Processing | VLSI Technology and Design | Digital Data Communication | Digital Control System | Digital System Design | -- | Transform Techniques | -- | -- |
| E C E VLSI & ES (68) | Embedded Systems Concepts | Hardware Software Co-Design | Embedded Software Design | Micro Controller of r Embedded System Design | RF & Micro Wave Integrated Circuits | VHDL Modeling of Digital Systems | --- | Analog And Digital IC Design | --- |

| Course | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| E C E VLSID & VLSD/VLSI (72 & 57) | Embedded Systems Concepts | VLSI Technology and Design | Elective – I Digital Data Communications | -- | Digital System Design | Elective-II VHDL Modeling of Digital Systems | Electronic Design Automation tools | Analog IC Design | -- |
| E E E POWER ELECTRONICS (43) | Machine Modeling and Analysis | Analysis of Power Electronic Converters | Power Electronic Control of DC Drives | -- | Modern Control Theory | **Elective-I** Energy Conversation Systems / Prog. Logic Controller & It's Applications | Modern Power Electronics | HVDC Transmissions | -- |
| E E E POWER ELECTRONICS AND ELECTRI DRIVES (54) | Machine Modeling and Analysis | Analysis of Power Electronic Converters | Power Electronic Control of DC Drives | Digital Control Systems | Operations Research | **Elective-I** Energy Conversation Systems / Neural & Fuzzy Systems | -- | -- | Microprocessor & Microcontrollers' |
| E E E POWER AND INDUSTRIAL DRIVES (42) | Machine Modeling and Analysis | Analysis of Power Electronic Converters | Power Electronic Control of DC Drives | Digital Control Systems | Operations Research | **Elective-I** Energy Conversation Systems / Neural & Fuzzy Systems | -- | -- | Microprocessor & Microcontrollers' |
| E E E POWER SYSTEMS HV ENGG | Advanced Control Systems | --- | Electrical Distributions System | High Voltage Engineering | -- | Dielectric and Insulation Engineering | Extra High Voltage Transmission | HVDC Transmissions | --- |
| E E E P.S. CONTROL AND AUTOMATION (53) | Distribution Automation | Power System Operation and Control | Advanced Power System Protection | Digital Control Systems | -- | -- | -- | HVDC Transmissions | Microprocessors & Microcontrollers |

NOTE: ( i )  If Government declares holiday on any of the above dates, the examinations will be conducted as usual

(ii )  Any omissions or clashes in this Time Table may please  be informed to the Controller of Examinations immediately.

(iii)  The Principals are requested to inform the University, if any other substitute subjects that are not included in the above list immediately.

*A.m. prasad*

**Date:28-03-2012**                                                                 **Controller of Examinations**