## G.B. TECHNICAL UNIVERSITY, LUCKNOW Study and Evaluation Scheme B. Tech. in Electronics & Communication Engineering [Effective from the session 2011-12]

## YEAR 4<sup>th</sup>, SEMESTER-VII

|                 | Course     |  |    |         |   |     | Evaluation Scheme  |           |     |         |       |
|-----------------|------------|--|----|---------|---|-----|--------------------|-----------|-----|---------|-------|
| S. No.          |            | e SUBJECT                              |    | PERIODS |   |     | SESSIONAL<br>EXAM. |           |     | Subject | redit |
|                 | Coue       |  | L  | Т       | Р | СТ  | TA                 | Tota<br>1 | Е   | Total   |       |
| THEORY SUBJECTS |            |  |    |         |   |     |                    |           |     |         |       |
| 1.              | EOE 07*    | Open Elective-I**                      | 3  | 1       | 0 | 30  | 20                 | 50        | 100 | 150     | 4     |
| 2.              | EOE 02*    | Departmental Elective-II               | 3  | 1       | 0 | 30  | 20                 | 50        | 100 | 150     | 4     |
| 3.              | EEC 701    | Optical Communication                  | 3  | 1       | 0 | 30  | 20                 | 50        | 100 | 150     | 4     |
| 4.              | EEC 702    | Data Communication Networks            | 3  | 1       | 0 | 30  | 20                 | 50        | 100 | 150     | 4     |
| 5.              | EEC 703    | VLSI Design                            | 3  | 1       | 0 | 30  | 20                 | 50        | 100 | 150     | 4     |
| 6.              | AUC<br>001 | *Human Values & Professional<br>Ethics | 2  | 0       | 0 | 15  | 10                 | 25        | 50  | 75      | -     |
| PRAC            | CTICAL/DE  | ESIGN/DRAWING                          |    |         |   |     |                    |           |     |         |       |
| 7.              | EEC 751    | Microwave & Fiber Optic Lab            | 0  | 0       | 2 | -   | 20                 | 20        | 30  | 50      | 1     |
| 8.              | EEC 752    | Electronics Circuit Design Lab         | 0  | 0       | 3 | -   | 50                 | 50        | -   | 50      | 2     |
| 9.              | EEC 753    | Industrial Training Viva-Voce          | 0  | 0       | 2 | -   | 20                 | 20        | 30  | 50      | 1     |
| 10.             | EEC 754    | Project                                | 0  | 0       | 2 | -   | 50                 | 50        | -   | 50      | 1     |
| 11.             | GP 701     | General Proficiency                    | -  | -       | - | -   | -                  | 50        | -   | 50      | 1     |
|                 |            | Total                                  | 15 | 5       | 9 | 150 | 240                | 440       | 560 | 1000    | 26    |

\*\* Open Electives-I

EOE-071 Entrepreneurship Development EOE-072 Quality Management EOE-073 Operation Research EOE-074 Introduction to Biotechnology EOE-075 Micro and smart systems

### G.B. TECHNICAL UNIVERSITY, LUCKNOW Study and Evaluation Scheme B. Tech. in Electronics & Communication Engineering [Effective from the session 2011-12]

# YEAR 4<sup>th</sup>, SEMESTER-VIII

|                 | Course<br>Code |  |    |         |    | <b>Evaluation Scheme</b> |                    |           |     |         |       |
|-----------------|----------------|--|----|---------|----|--------------------------|--------------------|-----------|-----|---------|-------|
| S. No.          |                | Course SUBJECT                         | PE | PERIODS |    |                          | SESSIONAL<br>EXAM. |           |     | Subject | redit |
|                 |                |  | L  | Т       | Р  | СТ                       | TA                 | Tota<br>1 | Е   | Total   | 0     |
| THEORY SUBJECTS |                |  |    |         |    |                          |                    |           |     | I       |       |
| 1.              | EOE 08*        | Open Elective-II**                     | 3  | 1       | 0  | 30                       | 20                 | 50        | 100 | 150     | 4     |
| 2.              | EEC 03*        | Departmental Elective-III              | 3  | 1       | 0  | 30                       | 20                 | 50        | 100 | 150     | 4     |
| 3.              | EEC 801        | Wireless & Mobile<br>Communication     | 3  | 1       | 0  | 30                       | 20                 | 50        | 100 | 150     | 4     |
| 4.              | EEC 802        | Electronics Switching                  | 3  | 1       | 0  | 30                       | 20                 | 50        | 100 | 150     | 3     |
| 5.              | AUC 001        | *Human Values &<br>Professional Ethics | 2  | 0       | 0  | 15                       | 10                 | 25        | 50  | 75      | -     |
| PRAC            | TICAL/DE       | SIGN/DRAWING                           | •  | •       |    |                          |                    |           |     |         |       |
| 6.              | EEC 851        | Project                                | 0  | 0       | 12 | -                        | 100                | 100       | 250 | 350     | 8     |
| 7.              | GP 801         | General Proficiency                    | -  | -       | -  | -                        | -                  | 50        | -   | 50      | 1     |
|                 |                | Total                                  | 12 | 4       | 12 | 120                      | 180                | 350       | 650 | 1000    | 24    |

\*\* Open Electives-II

EOE-081 Non Conventional Energy Resources EOE-082 Nonlinear Dynamic system EOE-083 Product Development EOE-084 Automation and Robotics

### LIST OF ELECTIVES:

#### Elective – I

| 1. | EEC 011 | Analog Signal Processing      |
|----|---------|-------------------------------|
| 2. | EEC 012 | Data Structure                |
| 3. | EEC 013 | Advance Semiconductor Devices |
| 4. | EEC 014 | Microcontrollers              |

#### Elective – II

| 1. | EEC 021 | Satellite Communication  |
|----|---------|--------------------------|
| 2. | EEC 022 | Digital Image Processing |
| 3. | EEC 023 | ANN                      |
| 4. | EEC 024 | Filter Design            |

#### Elective – III

| 1. | EEC 031 | Optical Networks                 |
|----|---------|----------------------------------|
| 2. | EEC 032 | Digital System Design using VHDL |
| 3. | EEC 033 | Speech Processing                |
| 4. | EEC 034 | Integrated Circuit Technology    |
| 5. | EEC 035 | Introduction to RADAR systems    |

|      | STLLADUS  |          |
|------|---|----------|
|      | EEC 701 OPTICAL COMMUNICATION   | 3 1 0    |
| UNIT | TOPICS  | LECTURES |
| Ι    | Overview of optical fiber communication-                                      | 8        |
|      | The general system, advantages of optical fiber communications. Optical fiber |          |
|      | wave guides- Introduction, Ray theory transmission, Optical fiber Modes and   |          |
|      | Configuration, Mode theory for circular waveguides, Step Index fibers,        |          |
|      | Single mode fibers. Cut off wavelength Mode Field Diameter Effective          |          |
|      | Refractive Index Fiber Material and its Fabrication Techniques                |          |
| П    | Signal distortion in optical fibers. Attenuation Absorption Scattering and    | 8        |
| 11   | Bonding losses Core and Cladding losses Information consoity                  | 0        |
|      | determination Group delay Attenuation Massurements Techniques                 |          |
|      | Types of Dispersion Meterial dispersion Wave guide dispersion                 |          |
|      | Palarization mode dispersion Intermodel dispersion, Pulse breadening          |          |
|      | Overell fiber dispersion in Multi mode and Single mode fibers.                |          |
|      | dispersion massurement techniques. Non linear offacts                         |          |
|      | Optical fiber Connectors: Jointa Counters and Isolators                       |          |
| ш    | Optical fiber Connectors, Jonnis, Couplers and Isolators.                     | 0        |
| 111  | Modulation Dowar handwidth product  | 0        |
|      | Modulation, Power bandwidth product.  |          |
|      | Laser Diodes- Basic concepts, Classifications, Semiconductor injection Laser. |          |
|      | Modes, Infestional conditions, External quantum efficiency, Laser diode rate  |          |
| IV.  | equations, resonant frequencies, renability of LED & ILD                      | 0        |
| IV   | Source to fiber power launching - Output patterns, Power coupling, Power      | 8        |
|      | launching, Equilibrium Numerical Aperture, Laser diode to fiber coupling.     |          |
|      | Optical detectors- Physical principles of PIN and APD, Detector response      |          |
|      | time, Temperature effect on Avalanche gain, Comparison of Photo detectors.    |          |
|      | Optical receiver operation- Fundamental receiver operation, Digital signal    |          |
|      | transmission, error sources, Receiver configuration, Digital receiver         |          |
|      | performance, Probability of error, Quantum limit, Analog receivers            |          |
| V    | Link Design: Doint to Point Links Dower Deputition Error                      | 8        |
| v    | control Multichannel Transmission Techniques WDM concepts and                 | 0        |
|      | component overview  |          |
|      | OTDP and optical Power mater  |          |
|      | OTDK and optical Power meter  |          |

## SVLL ARUS

## **TEXT BOOKS:**

- John M. Senior, "Optical Fiber Communications", PEARSON, 3<sup>rd</sup> Edition, 2010.
   Gerd Keiser, "Optical Fiber Communications", TMH, 4<sup>th</sup> Edition, 2008.

## **REFERENCE BOOKS**

1. Govind P. Agrawal, "Fiber Optic Communication Systems", John Wiley, 3<sup>rd</sup> Edition, 2004.

2. Joseph C. Plais, "Fiber Optic Communication", Pearson Education, 4<sup>th</sup> Ed, 2004.

|      | EEC 702 DATA COMMUNICATION NETWORKS   | 3 1 0    |
|------|---|----------|
| Unit | Торіс   | Lectures |
| Ι    | Introduction to Networks & Data Communications<br>The Internet, Protocols & Standards, Layered Tasks, OSI Model, TCP / IP,<br>Addressing, Line Coding Review, Transmission Media: Guided and<br>unguided Media Review.  | 8        |
| II   | Switching: Datagram Networks, Virtual Circuit Networks, Structure of a<br>switch ,Ethernet Physical Layer, Data Link Layer: Error detection and<br>Correction Data Link Control: Framing, Flow and Error Control Protocols,<br>Noiseless Channel and Noisy Channel Protocol, HDLC, Point-to-Point<br>Protocol | 8        |
| III  | Multiple Access : RANDOH, CDMA, CSMA/CD, CSMA/CA, Controlled<br>Access, Channelization<br>Wired LANs: IEEE Standards, Standard Ethernet, Fast Ethernet, Gigabit<br>Ethernet, Wireless LAN<br>IEEE 802.11, Bluetooth IEEE 802.16   | 8        |
| IV   | Network Layer : Design Issues. Routing Algorithms. Congestion control<br>Algorithms.IPV4 Addresses, Connecting Devices, Virtual LAN IPV6<br>Addresses, Internet Protocol, Hardware Addressing versus IP Addressing,<br>IP Data Gram   | 8        |
| V    | Transport Layer Protocol : UDP and TCP, ATM<br>ATM, Cryptography, Network Security  | 8        |

1. B. A. Forouzan, "Data Communications and Networking", MGH, 4th ed. 2007 **Reference Books:** 

- A. S. Tanenbaum, "Computer Networks", PHI.
   W. Stallings, "Data and Computer Communication", PHI.

|      | EEC 703 VLSI DESIGN   | 3 1 0    |
|------|---|----------|
| Unit | Topic   | Lectures |
| Ι    | Introduction: Overview of VLSI Design Methodologies, VLSI Design Flow,<br>Design Hierarchy, Concepts of Regularity, Modularity and Locality.<br>MOSFET Fabrication: Fabrication process flow, NMOS and CMOS<br>fabrication, layout design rules, stick diagram and mask layout design.<br>MOS Transistor : MOS Structure, The MOS System under external bias,<br>Operation of MOSFET, MOSFET - Current /Voltage Characteristics, Scaling<br>and Small geometry effects and capacitances | 8        |
| II   | MOS Inverters: Introduction, Resistive Load Inverter, Inverters with n-type<br>MOSFET load, CMOS Inverter.<br>MOS Inverters - Switching Characteristics: Introduction, Delay – Time<br>Definitions, Calculation of Delay Times, and Inverter Design with Delay<br>Constraints.  | 8        |
| III  | Combinational MOS Logic Circuits: Introduction, MOS logic circuits with<br>depletion NMOS Loads, CMOS logic circuits, complex logic circuits, CMOS<br>transmission gates (pass gates)<br>Sequential MOS Logic Circuits: Introduction, behaviour bistable elements,<br>SR latch circuits, clocked latch and FF circuits, CMOS D latch and edge<br>triggered FF.  | 8        |
| IV   | Dynamic logic circuits: Introduction, basic principle of pass transistor<br>circuits, synchronous dynamic circuit techniques, dynamic CMOS circuit<br>techniques, domino CMOS logic.<br>Semiconductor memories: Introduction, DRAM, SRAM, ROM, flash<br>memory.   | 8        |
| V    | Low – Power CMOS Logic Circuits: Introduction, Overview of Power<br>Consumption, Low – Power Design through voltage scaling, Estimation and<br>Optimization of switching activity, Reduction of Switched Capacitance and<br>Adiabatic Logic Circuits.<br>Design for Testability: Introduction, Fault Types and Models, Controllability<br>and Observability, Ad Hoc Testable Design Techniques, Scan Based and<br>BIST Techniques   | 8        |

1. Sung-Mo Kang & Yosuf Leblebici, "CMOS Digital Integrated Circuits: Analysis & Design", TMH, 3<sup>rd</sup> Edition.

## **Reference Books:**

- D. A. Pucknell and K. Eshraghian, "Basic VLSI Design: Systems and Circuits", PHI, 3<sup>rd</sup> Ed., 1994.
- 3. W.Wolf, Modern VLSI Design: System on Chip, Third Edition, Pearson, 2002.

## **ELECTIVES II**

|      | EEC 021 SATELLITE COMMUNICATIONS   | 3 1 0    |
|------|--|----------|
| Unit | Торіс  | Lectures |
| Ι    | Elements of Satellite Communication. Orbital mechanics, look angle and         | 8        |
|      | orbit determination, launches & launch vehicle, orbital effects,               |          |
|      | Geostationary Orbit.   |          |
| II   | Satellite subsystems, attitude and orbit control systems, TTC&M,               | 8        |
|      | communication subsystem, satellite antenna                                     |          |
|      | Satellite link design: basic transmission theory, system noise temperature     |          |
|      | and G/T ratio, downlink design, uplink design, satellite systems using         |          |
|      | small earth station, design for specified C/N.                                 |          |
| III  | Propagation effects and their impact on satellite-earth links: attenuation and | 8        |
|      | depolarization, atmospheric absorption, rain, cloud and ice effects etc.       |          |
|      | Introduction of various satellite systems: VSAT, low earth orbit and non-      |          |
|      | geostationary,   |          |
| IV   | Direct broadcast satellite television and radio, satellite navigation and the  | 8        |
|      | global positioning systems, GPS position location principle, GPS Receivers     |          |
|      | and Codes, Satellite Signal Acquisition, GPS Navigation Message, GPS           |          |
|      | Signal Levels, Timing accuracy, GPS Receiver Operation                         |          |
| V    | Global Mobile Satellite Systems, Antenna System for mobile satellite           | 8        |
|      | applications, Evolution, Antenna Requirement and Technical                     |          |
|      | Characteristics, Classification of Mobile Satellite Antenna(MSA), Low gain     |          |
|      | omni directional Antenna, Medium gain Directional Antenna, High gain           |          |
|      | Directional Aperture Antenna, Wire Quadrifilar Helix Antenna(WQHA) for         |          |
|      | Hand held Terminals, Antenna Systems for Mobile Satellite Broadcasting.        |          |

Text/ Reference Books:

- B. Pratt, A. Bostian, "Satellite Communications", Wiley India.
   D. Roddy, "Satellite Communications", TMH, 4<sup>th</sup> Ed.
   S. D. Ilcev, "Global Mobile Satellite Communication", Springer
- 4. R. Pandya, "Mobile and Personal Communication Systems and Services", PHI.

| EEC 022 DIGITAL IMAGE PROCESSING |   |          |
|----------------------------------|---|----------|
| Unit                             | Торіс   | Lectures |
| I & II                           | Introduction: Fundamental steps in DIP, elements of DIP, Simple<br>image model, sampling & quantization, basic relationships between<br>pixels, colour image model.<br>Image Transforms: One-dimensional & two-dimensional DFT, cosine,<br>sine, Hadamard, Haar, and Slant & KL transforms.<br>Image Enhancement: Introduction, point operations, histogram<br>modelling, spatial operations, Transform operations. | 8        |
| III                              | Image Restoration: Introduction, image observation models, Inverse &<br>Wiener filtering, difference between enhancement & restoration<br>Restoration-spatial filtering, Noise reduction in frequency domain.   | 8        |
| IV                               | Image Compression: Introduction, Pixel coding, Predictive coding,<br>Transform coding, Inter-frame coding   | 8        |
| V                                | Image Segmentation: Introduction, Spatial feature extraction, Transforms features, Edge detection, Boundary extraction, Segmentation techniques.  | 8        |

 Rafael C. Gonzalez Richard E Woods, "Digital Image Processing", Pearson, 3<sup>rd</sup> Ed. 2009.

2. Anil K Jain, "Fundamentals of Digital Image Processing", PHI.

|      | EEC 023 Artificial Neural Networks  | 3 1 0    |
|------|---|----------|
| Unit | Topic   | Lectures |
| Ι    | Introduction:<br>Introduction and history, human brain, biological neuron, models of<br>neuron, signal flow graph of neuron, feedback, network architecture,<br>knowledge representation, Artificial intelligence and neural networks.<br>Learning Process:   | 4        |
|      | Error correction learning, memory based learning, Hebbian<br>learning, competitive learning, Boltzmann learning, learning with and<br>without teacher, learning tasks, memory and adaptation.   |          |
| II   | Artificial neurons, Neural networks and architectures<br>Introduction, neuron signal function, mathematical preliminaries, Feed<br>forward & feedback architecture.   | 2        |
|      | Geometry of Binary threshold neurons and their networks<br>Pattern recognition, convex sets and convex hulls, space of<br>Boolean functions, binary neurons for pattern classification, non linear<br>separable problems, capacity of TLN, XOR solution.  | 3        |
| III  | Perceptrons and LMS<br>Learning objective of TLN, pattern space & weight space,<br>perceptron learning algorithm, perceptron convergence theorem, pocket<br>algorithm, $\alpha$ – LMS learning, MSE error surface, steepest descent search,<br>$\mu$ – LMS and application.<br>Back propagation and other learning algorithms<br>Multilayered architecture, back propagation learning algorithm,<br>practical considerations, structure growing algorithms, applications of<br>feed forward neural networks, reinforcement learning | 5        |
| IV   | Statistical Pattern Recognition<br>Bayes' theorem, classical decisions with Bayes' theorem,<br>probabilistic interpretation of neuron function, interpreting neuron<br>signals as probabilities, multilayered networks & posterior<br>probabilities, error functions for classification problems.<br>RBF Networks<br>Regularization networks, generalized RBF networks, RBF network<br>for solving XOR problem, comparison of RBF networks & multilayer<br>perceptrons.   | 4        |
|      | Stochastic Machines<br>Statistical mechanics, simulated annealing, Boltzmann machine.   | 2        |
| V    | Adaptive Resonance Theory<br>Building blocks of adaptive resonance, Adaptive Resonance Theory 1.<br>Self Organizing Feature MAP<br>Introduction, Maximal eigenvector filtering, principal component analysis,<br>generalized learning laws, competitive learning, vector quantization,<br>Mexican hat networks.   | 8        |

- Kumar Satish, "Neural Networks", TMH
   Simon Haykin, "Neural Networks", PHI

|      | EEC 024 FILTER DESIGN  | 3 1 0    |
|------|--|----------|
| Unit | Торіс  | Lectures |
| Ι    | Review of op-amps circuits, Categorization of filters-Low-pass filter,       | 8        |
|      | High-pass filter, band-pass filter, band-reject filter, Gain equalizers, and |          |
|      | Delay equalizers.  |          |
| II   | Approximation Theory: Butterworth approximation, Chebyshev                   | 8        |
|      | approximation, Inverse Chebyshev approximation, Basic of sensitivity,        |          |
|      | Frequency Transformations.   |          |
| III  | Three amplifier Biquad: Basic low pass and band pass circuit,                | 10       |
|      | realization of the general Biquadratic Functions, summing of four            |          |
|      | Amplifier biquad, feed forward three amplifier biquad, Passive Ladder        |          |
|      | structures, Inductor Substitution using Gyrator, Transformation of           |          |
|      | elements using the FDNR. Active ladder filters. Active R filters.            |          |
| IV   | Elementary transconductor building blocks, resistors, integrators,           | 8        |
|      | filters.   |          |
| V    | Switched capacitor filters: The MOS switch, The switched capacitor,          | 6        |
|      | first order building blocks, second order sections, sampled data             |          |
|      | operation, Switched capacitor first and second order filters, Bilinear       |          |
|      | transformation based SC filter design.                                       |          |

[1] Gobind Daryanani, "Principles of active network synthesis and design", John Wiley & Sons.

[2] R. Schaumann, M. E. Van Valkenburg, "Design of analog filters", Oxford University Press.

## EEC 751 Microwave and Optical Communication Lab

Minimum Ten Experiments to be conducted:

Part – A (Any 6 Experiments):

- 1. Study of Reflex Klystron Characteristics.
- 2 Measurement of guide wavelength and frequency of the signal in a rectangular Waveguide using slotted line carriage in a Micro wave Bench.
- 3. Measurement of impedance of an unknown load connected at the output end of the slotted line carriage in a Micro wave Bench
- 4. Determine the S-parameter of any Three port Tee.
- 5 Determine the S-parameter of a Magic Tee.
- 6. Study various parameters of Isolator .
- 7. Measurement of attenuation of a attenuator and isolation, insertion loss, cross coupling of a circulator.
- 8 Determine coupling coefficient, Insertion loss, Directivity and Isolation coefficient of anty Multi-Hole directional coupler.
- 9 To study working of MIC Components like Micro strip Line, Filter, Directional Coupler, Wilkinson Power Divider, Ring resonator & coupler, antennas & amplifies.
- 10. Study of waveguide horn and its radiation pattern and determination of the beam width.
- 11. Study radiation pattern of any two types of linear antenna.

Part – B (Any 4 Experiments):

- 1. To setting up fiber optic analog link.
- 2. Study and measurement of losses in optical fiber.
- 3. Study and measurement of numerical aperture of optical fiber.
- 4. Study and perform time division multiplexing (digital).
- 5. Study of framing in time division multiplexing.
- 6. Study of Manchester coding and decoding.
- 7. Study of voice coding and codec chip.
- 8. Study and measure characteristics of fiber optic LED's and photo detector.

## EEC 752 Electronic Circuit Design

In this practical course students will carry out a design oriented project work using various analog/ digital building blocks which they have already studied in their analog electronic/ digital electronic courses such as Electronic circuits, integrated circuits and filter design. The project may include but not restricted to any of the following:

- 1. Universal op-amp based biquad
- 2. Universal OTA biquad
- 3. Amplitude control or stabilization applied to any sinusoidal oscillators
- 4. Op-amp/ OTA based function generator
- 5. Any application of log/antilog circuits
- 6. Any applications of analog multiplier/ divider
- 7. Any digital system design and its hardware implementation using TTL/ CMOS ICs
- 8. Any circuit idea (not studied in the course) using 555 Timer in conjunction with any other ICs

The above must include

- 1. Design the circuit.
- 2. Make a hardware and measure various parameters.
- 3. Simulation in Spice of the designed circuit.
- 4. Comparison of measured and simulated results.
- 5. A report is to be made for evaluation.

| EEC 801 Mobile and Wireless Communication |  | 3 1 0    |
|---|--|----------|
| Unit                                      | Торіс  | Lectures |
| Ι   | Evolution of mobile radio communication fundamentals.                    | 8        |
|   | Large scale path loss: propagation models, reflection, diffraction,      |          |
|   | scattering, practical link budget design using path loss model.          |          |
|   | Small scale fading & multipath propagation and measurements,             |          |
|   | impulse response model and parameters of multipath channels.             |          |
|   | Small scale Multipath Measurements, Parameters of Mobile Multipath       |          |
|   | Channels types of small scale fading.                                    |          |
| II  | Fundamentals of equalisation, Equalisers in communication receiver,      | 8        |
|   | Survey of equalisation techniques, linear equaliser, Algorithms for      |          |
|   | Adaptive Equalization, Diversity techniques, RAKE receiver.              |          |
|   | Characteristics of speech signals, quantisation techniques, vocoders,    |          |
|   | linear predictive coders, Multiple Access techniques for Wireless        |          |
|   | Communications.  |          |
| III                                       | Cellular concepts, Frequency reuse, channel assignment strategies,       | 8        |
|   | handoff strategies, interference and system capacity, improving coverage |          |
|   | and capacity in cellular systems.  |          |
| IV  | GSM system for mobile: Services and features, System Architecture, Radio | 8        |
|   | Sub system Channel types, Frame Structure.                               |          |
|   | CDMA Digital Cellular Standard (IS 95): Frequency and Channel            |          |
|   | specifications, Forward CDMA channel and reverse CDMA channel            |          |
| V   | Introduction to Mobile Adhoc Networks, Mobile data networks, wireless    | 8        |
|   | standards IMT2000, Introduction to 4G and concept of NGN.                |          |

- 1. T.S. Rappaport, "Wireless Communication-Principles and practice", Pearson, Second Edition.
- T L Singal ,"Wireless Communications ",McGraw Hill Publications.
   R. Pandya, "Mobile and personal communication system", PHI.

#### **Reference Books:**

- Andrea Goldsmith, "Wireless Communications", Cambridge University press. 1.
- Andreas F. Molisch, "Wireless Communications", Wiley Student Edition. 2.
- 3. S. Haykin & M. Moher, "Modern wireless communication", Pearson, 2005.

|      | EEC 802 ELECTRONIC SWITCHING   |                          | 3 1 0    |
|------|--|--------------------------|----------|
| Unit | Торіс  | Text<br>Book/<br>Chapter | Lectures |
| Ι    | Evolution of Switching systems: Introduction: Message  | 2/3                      | 8        |
|      | switching, circuits switching, functions of a switching system,  |                          |          |
|      | switch, a general trunking, electronic switching, Reed   |                          |          |
|      | electronic system, digital switching systems.  |                          |          |
| II   | Digital switching: Switching functions, space division   | 3/5                      | 8        |
|      | switching, Time division switching, two dimensional  |                          |          |
|      | in analog environment.   |                          |          |
| III  | Telecom Traffic Engineering: Network traffic load  | 1/8                      | 8        |
|      | and parameters, grade of service and blocking probability,   |                          |          |
|      | modelling switching systems, incoming traffic and  |                          |          |
|      | service time characterization, blocking models and loss estimates Delay systems                        |                          |          |
| IV   | Control of Switching Systems: Introduction, Call   | 2/7                      | 8        |
|      | processing functions; common control, Reliability<br>availability and security: Stored program control |                          |          |
|      | Signalling: Introduction, Customer line signalling, AF   | 2/8                      |          |
|      | junctions and trunk circuits, FDM carrier systems, PCM and   |                          |          |
|      | inter register signalling, Common channel signalling   |                          |          |
|      | principles, CCITT signalling system No. 6 and /, Digital   |                          |          |
| V    | Packet Switching: Packets formats, statistical multiplexing.   | 3/10                     | 8        |
|      | routing control, dynamic, virtual path circuit and fixed path  |                          |          |
|      | routing, flow control, X.25 protocol, frame relay, TCP/IP,   |                          |          |
|      | ATM cell, ATM service categories, ATM switching, ATM   |                          |          |
|      | memory switch, space memory switch, memory-space, memory-space-memory switch, Banyan network switch.   |                          |          |

- 1. Thiagarajan Viswanathan, "Telecommunication switching System and networks", PHI.
- 2. J.E. Flood, "Telecommunication switching, Traffic and Networks", Pearson education.
- 3. J.C. Bellamy, "Digital Telephony", John Wiley, 3<sup>rd</sup> Ed.

## **ELECTIVE III**

| EEC 031 OPTICAL NETWORKS |  | 3 1 0    |
|--------------------------|--|----------|
| Unit                     | Торіс  | Lectures |
| Ι                        | Introduction to Optical Networks- Principles and Challenges and its<br>Generation, Characteristics of Optical Fiber in non linear region ,Optical<br>Packet Switching, Transmission Basics, Multiplexers & Filters,  | 8        |
| II                       | Optical Amplifiers ,Tunable Lasers, Switches, Wavelength Converters.<br>Sub-Carrier Modulation and Multiplexing,Spectral<br>efficiency,Crosstalk,Introduction of Soliton systems.  | 8        |
| III                      | <ul> <li>SONET/SDH: Multiplexing, SONET/ SDH Layers,<br/>Frame Structure, Physical Layer, Elements of a SONET/SDH<br/>Infrastructure, Ethernet.</li> <li>Optical Transport Network, Generic framing Procedure, IP routing and<br/>forwarding and QOS.</li> <li>WDM Network Elements<br/>Optical Line Terminals, Optical Line Amplifiers,</li> </ul>  | 8        |
|                          | Optical Add/ Drop Multiplexers, Optical Cross  |          |
|                          | Connects.  |          |
| IV                       | WDM Network Design<br>Cost Trade-offs, Light path Topology Design, and<br>Routing and wavelength assignment problems, Dimensioning<br>Wavelength Routing Networks,<br>Network Survivability<br>Basic Concepts, Protection in SONET/SDH,<br>Protection in client layer, Optical Layer Protection, Different Schemes,<br>Interworking between Layers<br>Access Networks<br>Network Architecture Overview, Enhanced HFC,<br>FTTC, PON evolution | 8        |
| V                        | Optical Switching<br>OTDM, Synchronization, Header Processing,<br>Buffering, Burst Switching.<br>Deployment Considerations- SONET/SDH core Network   |          |

- Text Books:
  1. R. Ramaswami, & K. N. Sivarajan, "Optical Networks a Practical perspective", Morgan Kaufmann Publishers, 3<sup>rd</sup> Ed.
  2. U. Black, "Optical Networks: Third Generation Transport Systems"/ Pearson Educations
  Reference Books:

1. Biswanath Mukherjee "Optical WDM Networks" Springer Pub 2006.

| UnitTopicLecturesIIntroduction to VHDL, reserve words, structures, modeling, objects, data<br>type and operators, sequential statements and processes, sequential<br>modeling and attributes, conditional assignment, concatenation and case,<br>array loops and assert statements, subprograms.8IIDigital System Design Automation- Abstraction Levels, System level<br>design flow, RTL design flow, VHDL.<br>RTL Design with VHDL - Basic structures of VHDL, Combinational<br>circuits, Sequential circuits, Writing Test benches, Synthesis issues, VHDL<br>Essential Terminologies<br>VHDL Constructs for Structures and Hierarchy Descriptions - Basic<br>Components, Component Instantiations, Iterative networks, Binding<br>Alternatives, Association methods, generic Parameters, Design<br>Configuration8IIIConcurrent Constructs for RT level Descriptions - Concurrent Signal<br>Assignments, Guarded signal assignment<br>Sequential Constructs for RT level Descriptions - Process Statement,<br>Sequential WAIT statement, VHDL Subprograms, VHDL library Structure,<br>Packacing Utilities and Components, Sequential Statements8   |
|---|
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| Image: Section of Component Terminologies         VHDL Constructs for Structures and Hierarchy Descriptions – Basic         Components, Component Instantiations, Iterative networks, Binding         Alternatives, Association methods, generic Parameters, Design         Configuration         III         Concurrent Constructs for RT level Descriptions – Concurrent Signal         Assignments, Guarded signal assignment         Sequential Constructs for RT level Descriptions – Process Statement,         Sequential WAIT statement, VHDL Subprograms, VHDL library Structure,         Packaging Utilities and Components, Sequential Statements  |
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| Packaging Utilities and Components Sequential Statements  |
| rackaging curities and components, sequential statements.   |
| VHDL language Utilities - Type Declarations and Usage, VHDL   |
| Operators, Operator and Subprogram overloading, Other TYPES and TYPE  |
| - related issues, Predefined Attributes   |
| IV     VHDL Signal Model – Characterizing hardware languages, Signal     8       Assignments     Consument on d Seguential Assignments     Multiple Consument   |
| Assignments, Concurrent and Sequential Assignments, Multiple Concurrent   |
| V Herdware Cores and Models Synthesis rules and styles Memory and 8   |
| V         Haldwale Coles and Models - Synthesis rules and styles, Memory and         8           Oueue Structures: Arithmetic Cores: Components with Separate Control         8   |
| and Data parts  |
| Core Design Test and Testability - Issues Related to Design Test Simple   |
| Test benches.   |

TEXT BOOKS:

 Z. Navabi, "VHDL-Modular Design and Synthesis of cores and Systems", TMH – 3<sup>rd</sup> Edition.
 R.D.M. Hunter, T. T. Johnson, "Introduction to VHDL" Spriger Publication, 2010. **REFERENCE BOOKS**:

C. H. Roth, "Digital System Design using VHDL", PWS Publishing
 Douglas Perry, "VHDL- Programming by examples", MGH

| EEC 033 SPEECH PROCESSING |  |          |
|---------------------------|--|----------|
| Unit                      | Торіс  | Lectures |
| Ι                         | Digital models for speech signals: Mechanism of speech production & acoustic phonetics, the acoustic theory of speech production, lossless tube models, and digital models for speech signals.   | 10       |
| Π                         | Time Domain methods of speech sampling: Time dependent<br>processing of speech, short time energy and average magnitude, short<br>time average zero crossing rate, discrimination between speech&<br>silence, pitch period estimation using parallel processing, short time<br>autocorrelation function & AMDF, pitch period estimation using<br>autocorrelation function.   | 10       |
| III                       | Short time Fourier Analysis: Definition and properties, design of filter<br>banks, implementation of filter bank summation method using FFT,<br>spectrographic displays, pitch detection, analysis by synthesis phase,<br>vocoder and channel vocoder.   | 10       |
| IV                        | Homomorphic speech processing: Homomorphic system for<br>convolution, complex cepstrum of speech, pitch detection using<br>Homomorphic processing, formant estimation, Homomorphic vocoder.  | 6        |
| V                         | Linear Predictive Coding of Speech: Basic principles of linear<br>predictive analysis, the autocorrelation method, computation of the gain<br>for the model, solution of LPC equations for auto correlation method,<br>prediction error and normalized mean square error, frequency<br>domain interpretation of mean squared prediction error relation of linear<br>predictive analysis to lossless tube models, relation between various<br>speech parameters, synthesis of speech from linear predictive parameters,<br>application of LPC parameters. | 10       |

Text / Reference Books:

- 1. R. L. Rabiner & R.W. Schafer, "Digital Processing of speech signals", Pearson Education.
- 2. B. Gold and Nelson Morgon, "Speech and audio signal processing", Wiley India Edition, 2006.

| EEC 034 VLSI TECHNOLOGY |  | 3 1 0    |
|-------------------------|--|----------|
| Unit                    | Торіс  | Lectures |
| Ι                       | Introduction To IC Technology: SSI, MSI, LSI, VLSI Integrated Circuits       | 8        |
|                         | Crystal Growth and Wafer Preparation:  |          |
|                         | Electronic Grade Silicon, Czochralski Crystal Growth, Silicon Shaping,       |          |
|                         | Processing Considerations.   |          |
|                         | Epitaxy: Vapor – Phase Epitaxy, Molecular Beam Epitaxy, Silicon on           |          |
|                         | Insulators, Epitaxial Evaluation.  |          |
| II                      | Oxidation: Growth Kinetics, Thin Oxides, Oxidation Techniques and            | 8        |
|                         | Systems, Oxides Properties.  |          |
|                         | Lithography: Optical Lithography. Photo masks, Wet Chemical Etching.         |          |
|                         | Dielectric and Polysilicon Film Deposition:                                  |          |
|                         | Deposition Processes, Polysilicon, Silicon Dioxide, Silicon Nitride.         |          |
| III                     | Diffusion: Diffusion of Impurities in Silicon and Silicon Dioxide, Diffusion | 8        |
|                         | Equations, Diffusion Profiles, Diffusion Furnace, Solid, Liquid and          |          |
|                         | Gaseous Sources, Sheet Resistance and its Measurement.                       |          |
|                         | Ion-Implantation: Ion-Implantation Technique, Range Theory, Implantation     |          |
|                         | Equipment.   |          |
| IV                      | Metallization: :Metallization Application, Metallization Choices, Physical   | 8        |
|                         | Vapor Deposition, Vacuum Deposition, Sputtering Apparatus.                   |          |
|                         | Packaging of VLSI devices: Package Types, Packaging Design                   |          |
|                         | Consideration, VLSI Assembly Technologies, Package Fabrication               |          |
|                         | Technologies.  |          |
| V                       | VLSI Process Integration: Fundamental Considerations For IC Processing,      | 8        |
|                         | NMOS IC Technology, CMOS IC Technology, Bipolar IC Technology,               |          |
|                         | Monolithic and Hybrid Integrated Circuits, IC Fabrication                    |          |

1. S. M. Sze, "VLSI Technology", 2<sup>nd</sup> Edition, McGraw –Hill Publication.

Reference Books:

- 1. S.K. Ghandhi, "VLSI Fabrication Principles", 2<sup>nd</sup> Edition,. Willy-India Pvt. Ltd.
- 2. J. D. Plummer, M. D. Deal and Peter B. Griffin, "Silicon VLSI Technology: Fundamentals, practice and modelling", Pearson Education.
- 3. Stephen A. Campbell, "Fabrication Engineering at the micro and nano scale", Oxford Univ Press.

| EEC 035 INTRODUCTION TO RADAR SYSTEMS |  | 3 1 0    |
|---------------------------------------|--|----------|
| Unit                                  | Topic  | Lectures |
| Ι                                     | Introduction to Radar: Basic Radar, The Simply Form of the Radar<br>Equations, Radar Block Diagram, Radar Frequencies, Applications of<br>Radar.<br>The Radar Equation: Detection of Signals in Noise, Receiver Noise and<br>the Signal-to-Noise Ratio, Probabilities of Detection and False Alarm,<br>Integration of Radar Pulses, Radar Cross Section of Targets, Radar Cross-<br>Section of Targets, Radar Cross-Section Fluctuations, Transmitter Power,<br>Pulse Repetition Frequency, Antenna Parameters, System Losses,<br>Problems | 8        |
| II                                    | MTI and Pulse Doppler Radar: Introduction to Doppler and MTI Radar,<br>Delay-Line Cancelers, Staggered Pulse Repetition Frequencies, Doppler<br>Filter Banks, Digital MTI Processing, Moving Target Detector,<br>Limitations to MTI Performance.   | 8        |
| III                                   | Tracking Radar: Tracking with Radar, Mono pulse Tracking, Conical<br>Scan and Sequential Lobing, Limitations to tracking Accuracy, Low-<br>Angle Tracking, Tracking in Range, Other Tracking Radar Topics,<br>Comparison of Trackers, Automatic Tracking with Surveillance<br>Radars(ADT)  | 8        |
| IV                                    | Detection of Signals in Noise: Introduction, Detection Criteria, Detectors,<br>Automatic Detection, Integrators, Constant-False-Alarm Rate Receivers.  | 8        |
| V                                     | Information from Radar Signals: Basic Radar Measurements, Theoretical<br>Accuracy of Radar Measurements, Ambiguity Diagram, Pulse<br>Compression, Target Recognition, Land Clutter, Sea Clutter, Weather<br>Clutter  | 8        |

Text/ Reference Books:

- 1.
- Merrill I. Skolnik "Introduction to Radar Systems" Third Edition. J.C. Toomay , Paul J. Hannen "Principles of Radar" Third Edition. 2.