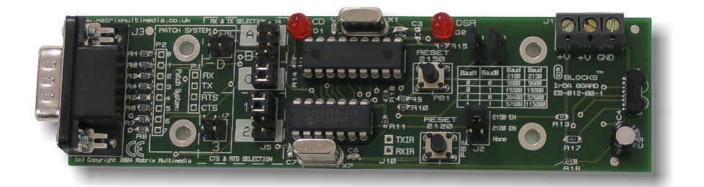
# IrDA Board datasheet





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Appendix

1 Circuit Diagram

## 1 About this document

This document concerns the Matrix IrDA Board code EB-012-00-1.

### Trademarks and Copyright

PIC, PICmicro are registered trademarks of Arizona Microchip Inc. E-blocks is a trademark of Matrix Multimedia Limited. EB-012-00-1 and associated software and documentation are Copyright ©2004 Matrix Multimedia Limited.

### Other sources of information

There are various other documents and sources that you may find useful:

#### Getting started with E-Blocks.pdf

This describes the E-blocks system and how it can be used to develop complete systems for learning electronics and for PICmicro programming.

#### PPP Help file

This describes the PPP software and its functionality. PPP software is used for transferring hex code to a PICmicro microcontroller.

### Disclaimer

The information in this document is correct at the time of going to press. Matrix Multimedia reserves the right to change specifications from time to time.

### Technical support

If you have any problems operating this product then please refer to the troubleshooting section of this document first. You will find the latest software updates, FAQs and other information on our web site: www.matrixmultimedia.co.uk. If you still have problems please email us at:

support@matrixmultimedia.co.uk. When emailing please state the operating system, the version of PPP you are using.

## 2 General information

This E-block allows investigation of IrDA standard wireless connectivity. This board can be used as a Secondary device for "point to point" applications, such as communication to a PalmOS® IrDA compatible device. It can also be used as a stand alone IrDA Encoder / Decoder. The board offers a range of user selectable baud rates. There is also a facility to directly access the infrared transceiver so that other infrared protocols can be investigated (e.g. television remote controls).

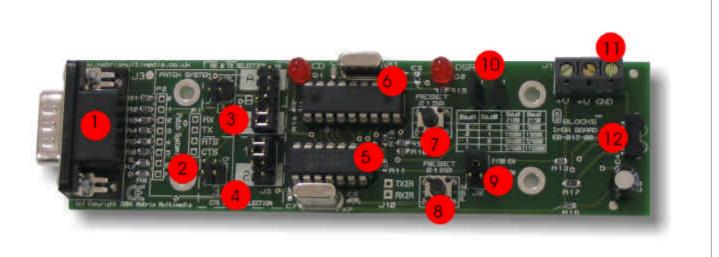
A set of jumper links are available which allow the IrDA E-block to easily be set for all PICmicro® microcontroller IrDA compatible devices. The patch system available on board makes this it compatible with numerous other devices.

Flowcode macros that make this device easier to use are available.

#### Features

- Operates as a "Point to Point" Secondary IrDA application
- Or a stand alone IrDA Encoder / Decoder
- Direct infrared transmission and reception also available
- User selectable Baud rate
- Flowcode macros available

# 3 IrDA Board Layout



Important: Please note that the mode selection jumpers are orientated in the correct way – i.e. with the metal connect strips HORIZONTAL as shown in the picture above.

- 1) 9-way downstream D-type connector
- 2) Patch system
- 3) RX & TX mode selection jumper pins
- 4) CTS & RTS mode selection jumper pins
- 5) MCP2120
- 6) MCP2150
- 7) Reset for MCP2150
- 8) Reset for MCP2120
- 9) MCP device enable jumper
- 10) Baud rate selection jumpers
- 11) Screw terminals
- 12) TFDU4100 serial infrared transceiver

#### General Guide for CTS and RTS settings

Jumper Settings	Description		
1	Hardware Flow Control ( $RX = bit 4$ , $TX = bit$		
2	No flow control		
3	Hardware flow control (Patch)		

#### General Guide for TX and RX settings

Jumper at	Jumper at B	Jumper at C		Jumper
А				at D
PIC16F88	PIC16F627(A)	PIC16F7x	PIC16C6x	Patch
PIC16F87	PIC16F628(A)	PIC16F7x7	PIC16CC7x	System
	PIC16F648A	PIC16F87x		
		PIC16F87xA		

For more information on see Section 4 – Circuit Description

# 4 Getting Started

As can be seen the circuit diagram (Appendix 1) consists of, which are: MCP2150, MCP2120, Connectors and a serial infrared transceiver. The following program will test all parts of this circuit. To test this board you will need to apply 5V to the IrDA Board via the screw terminal and then set the appropriate bits using the 9-Way D-type connector. The details of the IrDA configuration are stated in the Chapter 4 Circuit Description.

### Testing the IrDA Board - IrDA.hex

The following instructions explain the steps to test and use your IrDA Board. The instructions assume that PPP is installed and functional. It also assumes that you are confident in sending a program to the PIC via the Multiprogrammer.

This program was not available for this revision of the document. Please visit our E-blocks Members area at <u>www.matrixmultimedia.com/eblocks</u> Using the following user name and password User name: eblocks Password: halifax You will now have access to up-to-date information that you require.

### 4 Circuit description

The circuit as can be seen in the circuit diagram below (See Appendix 1 – Circuit diagram), made up sections: Connectors, MCP2150 circuitry, MCP2120 circuitry and an infrared transceiver circuitry.

The design of this product is to enable you to use this device with many standard PICmicro devices. This is achieved by identifying the PICmicro. Then by selecting the corresponding jumper setting on the IRDA board. This will configure the board with the correct pin-out for that particular device. Jumper setting A, B, C and D are used for selecting the appropriate pins for RX and TX. Jumper settings 1, 2 and 3 are used to set the correct pins for RTS and CTS. The following tables illustrate the correct jumper settings.

Jumper Setting	Jumper Setting	Jumper Setting		Jumper Setting
Α	В	PIC16F devices	PIC16C devices	D
PIC16F87	PIC16F627/A	PIC16F73	PIC16C63	PATCH SYSTEM
PIC16F88	PIC16F628/B	PIC16F737	PIC16CR63	
	PIC16F648A	PIC16F74	PIC16C65/A/B	
		PIC16F746	PIC16RC65	
		PIC16F76	PIC16C66	
		PIC16F767	PIC16C73/A/B	
		PIC16F77	PIC16C74/A/B	
		PIC16F777	PIC16C745	
		PIC16F870/1	PIC16C765	
		PIC16F873/A	PIC16C77	
		PIC16F874/A	PIC16C773	
		PIC16F876/A	PIC16C774	
		PIC16F877/A	PIC16C774	
CONNECT BOARD TO PORT B		CONNECT BO	ARD TO PORT C	

 Table 1. Jumper settings for TX and RX selection.

The following table (Table 2) shows the settings that can be used for CTS and RTS.

Jumpo	er Setting 1	Jumper Setting 2		Jumper Setting 3	
CTS	RTS	CTS	RTS	CTS	RTS
Bit 4	Bit 0	CTS not used	RTS not used	Patch	Patch

Table 2. Jumper settings for RTS and CTS selection

The Patch System allows the user to route TX, RX, CTS and RTS to any 8 of the bits that they require. This allows great flexibility, as the user can then use a different device other than specified in Table 1.

This board allows the user to select either the MCP2120 or the MCP2150 by placing enabling the device using J2 jumper block. This effectively enables one device and disables the other. By selecting "none" on the J2 jumper block, both devices can be disabled – this is required if direct connection to the infrared transceiver is required.

Both the MCP2120 and the MCP2150 have individual clocks. The board also enables the user to select the baud rate that is used. This is achieved using jumpers on J8 and J9. By having the jumper connected you select a 1 as input. The two jumpers create a binary input that sets the baud rate. The following table illustrates the settings and baud rates that are available:

Baud1 (J9)	Baud0 (J8)	Baud for MCP2120	Baud for MCP2150
0	0	9600	9600
0	1	19200	19200
1	0	38400	57600
1	1	57600	115200

The MCP2150 is compatible with the Physical layer of the IrDA standard. Therefore, this device uses six main I/Os, which are: RX, TX, CTS, RTS, TXIR and RXIR. RXIR and TXIR are the input or output from the serial infrared sensor. RX and TX are the communication to and from the controlling device, which could be a PICmicro® microcontroller. CTS and RTS are used as hardware flow control when communicating.

The MCP2120 uses only the bottom layer of the IrDA protocol, and is therefore used a stand alone Infrared Decoder / Encoder. The MCP2120 device only uses the RX, TX, RXIR and TXIR I/Os as described above. Thus there is no need to implement hardware flow control at this level the IrDA standard. This makes the operation quick and easy to use.

This E-block can also be used for investigating general infrared communication and prototyping such projects. This is required if, for example, you wish to develop a TV remote control unit or want to allow an existing remote control to communicate with your microcontroller. To do this, disable both the 2150 and 2120 devices by setting J2 to "none" and connect TXIR and RXIR of J10 to the appropriate port pin on the P2 patch block. We suggest that a 2-way SIL socket is soldered into J10.

### Appendix 1 - Circuit Diagram

